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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/661,872
Filed: September 12, 2003
Inventor(s):
Wong, et al.

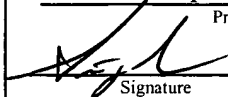
Examiner: Trong Q. Phan
Group/Art Unit: 2827
Atty. Dkt. No: 5681-69100

Title: SINGLE RANK MEMORY
MODULE FOR USE IN A
TWO-RANK MEMORY
MODULE SYSTEM

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Stephen J. Curran

Printed Name



Signature

5/4/05

Date

AMENDMENT; RESPONSE TO OFFICE ACTION OF
FEBRUARY 4, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of February 4, 2005, to further highlight why the application is in condition for allowance.